



Gokaraju Rangaraju Institute of Engineering & Technology (Autonomous)

Name of the college & Code : Gokaraju Rangaraju Institute of Engineering & Technology, 24

Name of the PG Program : Master of Technology

Specialization : VLSI (2019 Admitted Batch)

Academic Year & Semester : 2019-20, I Semester

| | 9.00 AM To 10.05 AM | 10.05 AM To 11.10 AM | 11.10 AM To 12.15 PM | 12.15 PM To 12.45 PM | 12.45 PM To 13.50 PM | 13.50 PM To 14.55 PM | 14.50 PM To 16.00 PM |
|------------|---------------------------|----------------------------|-------------------------------|----------------------------|-------------------------------|-------------------------------|-------------------------------|
| MON | RMIPR | DCD | Library /Faculty Meeting Hour | LUNCH | HDL Simulation Lab | | |
| TUE | RMIPR | DCD | VTD | | CMOS LAB | | |
| WED | DCD | DSD-HDL | Library /Faculty Meeting Hour | | HDL Simulation Lab | | |
| THU | VTD | AC-1 | AC-1 | | Library /Faculty Meeting Hour | DSD | DSD-HDL |
| FRI | CMOS LAB | | | | DSD-HDL | DSD | Library /Faculty Meeting Hour |
| SAT | DSD | VTD | Library /Faculty Meeting Hour | | Library /Faculty Meeting Hour | Library /Faculty Meeting Hour | Library /Faculty Meeting Hour |

| S.No. | Course Code: | Name of the Subject | Name of the Teacher |
|-------|--------------|---|---------------------------------|
| 1 | GR18D5076 | Digital System Design using HDL | Dr. P Asisa Kumar ID: 1566 |
| 2 | GR18D5077 | Digital CMOS Design | Dr. V Sreehari ID: 1444 |
| 3 | GR18D5078 | Digital System Design | Dr. D Jayanthi ID: 1495 |
| 4 | GR18D5083 | VLSI Technology and Design | Dr. D Lakshmi Chaitanya ID: 461 |
| 5 | GR18D5084 | HDL Simulation Lab | Mr. M Shankar Rao ID: 1529 |
| 6 | GR18D085 | Digital CMOS Design Lab | Dr. V Sreehari ID: 1444 |
| 7 | GR18D5012 | Research Methodology and IPR | Dr. J Sudharsan ID: 1564 |
| 8 | GR18D5207 | Audit Course-I (English for Research Paper Writing) | Dr. RL. Kanthi ID:718 |

M.Tech Coordinator

Dr.V.Sreehari

HOD

Dr. N. Swetha

Principal

Dr.J.Praveen